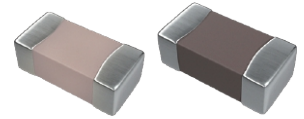


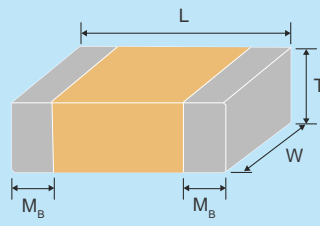
CT41 High Voltage Chip Multilayer Ceramic Capacitors General Purpose Series (4V to 4000V)



HOW TO ORDER

1206	B	104	K	500	C	R
Size	Dielectric	Capacitance	Tolerance	Rated voltage	Termination	Packaging style
Inch (mm) 0201 (0603) 0402 (1005) 0603 (1608) 0805 (2012) 1206 (3216) 1210 (3225) 1808 (4520) 1812 (4532) 1825 (4563) 2211 (5728) 2220 (5750) 2225 (5763)	N=NP0 B=X7R F=Y5V X=X5R	Two significant digits followed by no. of zeros. And R is in place of decimal point. eg.: 0R5=0.5pF 1R0=1.0pF 104=10x10 ⁴ =100nF	B=±0.1pF C=±0.25pF D=±0.5pF F=±1% G=±2% J=±5% K=±10% M=±20% Z=-20/+80%	Two significant digits followed by no. of zeros. And R is in place of decimal point. 4R0=4 VDC 6R3=6.3 VDC 100=10 VDC 160=16 VDC 250=25 VDC 500=50 VDC 101=100 VDC 102=1000 VDC 202= 2000 VDC	C=Cu/Ni/Sn	R = T&R

The Outlines and External Dimensions of Capacitors

Outline	Size Inch (mm)	L (mm)	W (mm)	T (mm)/Symbol	Soldering Method *	M _B (mm)	
	01R5 (0402)	0.4±0.02	0.2±0.02	0.2±0.02	V	R	0.10±0.03
	0201 (0603)	0.6±0.03	0.3±0.03	0.3±0.03	L	R	0.15±0.05
		0.6±0.05 ^{#2}	0.3±0.05 ^{#2}	0.3±0.05 ^{#2}			
	0402 (1005)	1.00±0.05	0.50±0.05	0.50±0.05	N	R	0.25 +0.05/-0.10
		1.00±0.20	0.50±0.20	0.5±0.20	Q	R	
	0603 (1608)	1.60±0.15/-0.10	0.80±0.10	0.80±0.07	S	R / W	0.40±0.15
				0.50±0.10	H	R / W	
				0.80±0.15/-0.10	X	R / W	
	0805 (2012)	2.00±0.15	1.25±0.10	0.8±0.20 ^{#3}	H	R / W	0.50±0.20
				0.60±0.10	A	R / W	
				0.80±0.10	B	R / W	
				1.25±0.10	D	R	
				0.85±0.10	T	R / W	
	1206 (3216)	3.20±0.20	1.60±0.20	1.25±0.20	I	R	0.60±0.20 (0.5±0.25) ^{***}
				0.80±0.10	B	R / W	
				0.95±0.10	C	R	
				1.25±0.10	D	R	
				1.15±0.15	J	R	
	1210 (3225)	3.20±0.40	2.50±0.30	1.60±0.20	G	R	0.75±0.25
				2.00±0.20	K	R	
2.50±0.30				M	R		
3.20+0.30/-0.10				P	R		
2.50±0.50 ^{#4}				M	R		
1808 (4520)	4.50±0.40 (4.5+0.5/-0.3) ^{**}	2.03±0.25	1.25±0.10	D	R	0.75±0.25 (0.5±0.25) ^{***}	
			1.40±0.15	F	R		
			1.60±0.20	G	R		
			2.00±0.20	K	R		
1812 (4532)	4.50±0.40 (4.5+0.5/-0.3) ^{**}	3.20±0.30	1.25±0.10	D	R	0.75±0.25 (0.5±0.25) ^{***}	
			1.60±0.20	G	R		
			2.00±0.20	K	R		
			2.50±0.30	M	R		
			2.80±0.30	U	R		
1825 (4563)	4.60±0.50	6.30±0.40	1.60±0.20 (G)	2.00±0.20 (K)	R	≥ 0.26	
2211 (5728)	5.70±0.50	2.80±0.30	2.50±0.30 (M)	2.80±0.30 (U)	R	≥ 0.30	
2220 (5750)	5.70±0.50	5.00±0.40			R	≥ 0.30	
2225 (5763)	5.70±0.50	6.30±0.40			R	≥ 0.30	

* R = Reflow soldering process ; W = Wave soldering process.

** For 1808_200V ~3kV, 1812_200V~3kV

*** For 1206_1000V ~3kV,1808_200V ~3kV, 1812_200V~3kV

#1 : For 0603/Cap ≥ 10μF or 0603(≤ 6.3V)/Cap ≥ 4.7μF For 0603(>10V)/Cap > 1μF products.

#2 : For 0201/Cap ≥ 0.68μF products.

#3 : For 0201/Cap ≥ 1μF products.

#4 : For 1210(100V)/Cap > 1μF or 1210(250V)/Cap > 0.47μF or 1210(400V~630V)/Cap > 0.22μF.

DESCRIPTION

MLCC consists of a conducting material and electrodes. To manufacture a chip-type SMT and achieve miniaturization, high density and high efficiency, ceramic condensers are used.

MLCC is made by NP0, X7R, X5R and Y5V dielectric material and which provides product with high electrical precision, stability and reliability.

FEATURES

- * A wide selection of sizes is available (0201 to 2225).
- * High capacitance in given case size.
- * RoHS REACH Compliance

APPLICATIONS

- For general digital circuit.
- For power supply bypass capacitors.
- For consumer electronics.
- For telecommunication.

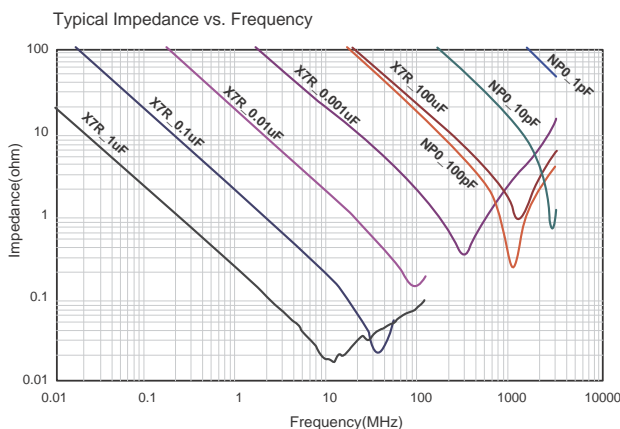
GENERAL ELECTRICAL DATA

Dielectric	NP0	X7R	X5R	Y5V
Size	0201, 0402, 0603, 0805, 1206, 1210, 1812, 1825, 2220, 2225			
Capacitance range	0.1pF to 0.27μF	100pF to 47μF	100pF to 220μF	0.01μF to 100μF
Capacitance tolerance	Cap≤5pF ^{#1} : A (±0.05pF), B (±0.1pF), C (±0.25pF) 5pF<Cap<10pF: C (±0.25pF), D (±0.5pF) Cap≥10pF: F(±1%), G (±2%), J (±5%), K (±10%)	J (±5%), K (±10%), M (±20%)	K (±10%), M (±20%)	M (±20%), Z (-20/+80%)
Rated voltage (WVDC)	10V, 16V, 25V, 50V, 100V	4V, 6.3V, 10V, 16V, 25V, 50V, 100V		
Operating temperature	-55 to +125°C		-55 to +85°C	-25 to +85°C
Capacitance characteristic	±30ppm	±15%	±15%	+30/-80%
Termination	Ni/Sn (lead-free termination)			

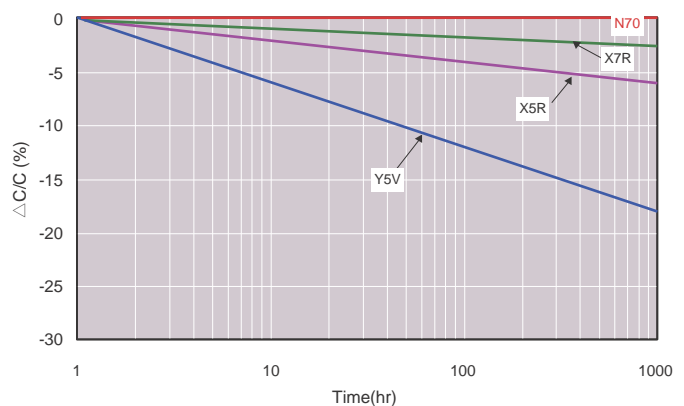
#1: NP0, 0.1pF product only provide B tolerance

ELECTRICAL CHARACTERISTICS

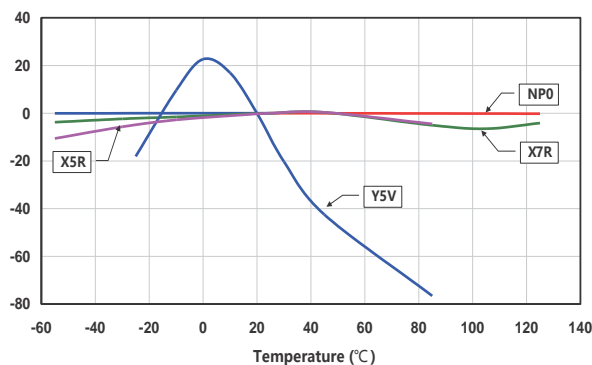
1) Frequency characteristics



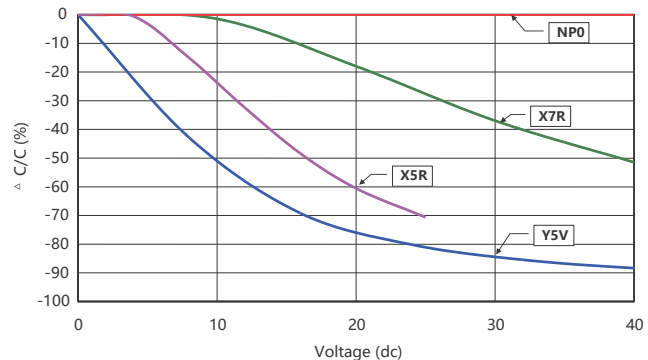
2) Capacitance Change - Typical aging rate



3) Temperature characteristics of capacitance (TCC)



4) DC Bias characteristics



All above typical electronic characteristics are for reference only.

Middle & High Voltage Capacitors 200V~4000V

■ FEATURES

- * High voltage in a given case size.
- * High stability and reliability.

■ GENERAL ELECTRICAL DATA

Dielectric	NP0	X7R	Y5V
Size	0603, 0805, 1206, 1210, 1808, 1812, 1825, 2220, 2225		0805, 1206, 1210, 1812
Capacitance	0.5pF to 0.12μF		0.01μF to 0.68μF
Capacitance tolerance	Cap≤5pF: C (±0.25pF) 5pF<Cap<10pF: D (±0.5pF) Cap≥10pF: J (±5%), K (±10%)		Z (-20/+80%)
Rated voltage (WVDC)	200V to 4000V		200V, 250V
DF/ Q	Cap<30pF: Q≥400+20C Cap≥30pF: Q≥1000		≤5%
Insulation resistance at Ur	Ur=200~630V: ≥10GΩ or RxC≥100Ω·F whichever is smaller Ur=1000~3000V: ≥10GΩ		
Dielectric strength	200~300V: ≥2 x WVDC 400V~450V: ≥1.2 x WVDC 500~999V: ≥1.5 x WVDC 1000~3000V: ≥1.2 x WVDC 4000: ≥1.1 x WVDC		
Operating temperature	-55 to +125°C		-25 to +85°C
Capacitance characteristic	±30ppm		+30/-80%
Termination	Ni/Sn (lead-free termination)		

■ CAPACITANCE RANGE

Y5V Dielectric 200V to 250V

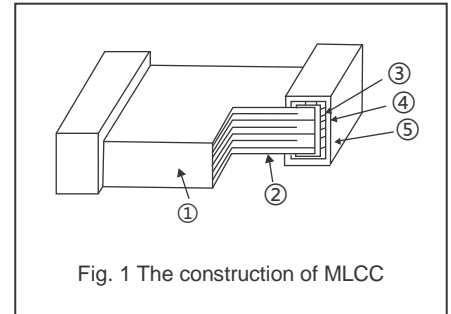
DIELECTRIC	Y5V								
	SIZE	0805		1206		1210		1812	
	RATED VOLTAGE (VDC)	200	250	200	250	200	250	200	250
Capacitance	0.010μF (103)	B	B	B	B	C	C	D	D
	0.015μF (153)	B	B	B	B	C	C	D	D
	0.022μF (223)	B	B	B	B	C	C	D	D
	0.033μF (333)	B	B	B	B	C	C	D	D
	0.047μF (473)	B	B	B	B	C	C	D	D
	0.068μF (683)	B	B	B	B	C	C	D	D
	0.10μF (104)			B	B	C	C	D	D
	0.15μF (154)			C	C	C	C	D	D
	0.22μF (224)							D	D
	0.33μF (334)							D	D
	0.47μF (474)							D	D
0.68μF (684)							D	D	

1. The letter in cell is expressed the symbol of product thickness.

General Information

■ Constructions

No.	Name	NP0	NPO/X7R/X5R/Y5V
①	Ceramic material	BaTiO ₃ based	
②	Inner electrode	AgPd alloy	Ni
③	Termination	Inner layer	Ag
④		Middle layer	Ni
⑤		Outer layer	Sn



■ Storage and handling conditions

- To store products at 5 to 40°C ambient temperature and 20 to 70% related humidity conditions.
- The product is recommended to be used within one year after shipment. Check solderability in case of shelf life extension is needed.

Cautions:

- The corrosive gas reacts on the terminal electrodes of capacitors, and results in the poor solderability. Do not store the capacitors in the ambience of corrosive gas (e.g., hydrogen sulfide, sulfur dioxide, chlorine, ammonia gas etc.)
- In corrosive atmosphere, solderability might be degraded, and silver migration might occur to cause low reliability.
- Due to the dewing by rapid humidity change, or the photochemical change of the terminal electrode by direct sunlight, the solderability and electrical performance may deteriorate. Do not store capacitors under direct sunlight or dewing condition. To store products on the shelf and avoid exposure to moisture.

■ Recommended soldering conditions

The lead-free termination MLCCs are not only to be used on SMT against lead-free solder paste, but also suitable against lead-containing solder paste. If the optimized solder joint is requested, increasing soldering time, temperature and concentration of N₂ within oven are recommended.

